

PCD6.M540

Features	Comments	P100 ≥ V.	P300 ≥ V.	P400 ≥ V.	FW ≥ V.	HW ≥ V.	Remarks
1.	<u>New instructions</u>						
CPBI	Call Program Block Indirect	003	1.7	1.4	002	x	
DEFTR	Define Timer Resolution	---	---	---	---	---	not implemented
PID	Instruction has been rewritten	---	---	---	---	---	not corrected, only the
	Selection of the old algorithm	---	---	---	---	---	old algorithm is used
SASII	New indirect instruction	---	---	---	---	---	not implemented
SYSCMP	System Compare	---	---	---	---	---	not implemented
SYSRD	System Read	---	---	---	---	---	not implemented
SYSWR	System Write	---	---	---	---	---	not implemented
TEST	New Test instruction	003	1.9	1.4	004	x	
TFR	Transfer Data	003	1.7	1.4	002	x	Data Blocks and Texts
TFRI	Transfer Data Indirect	003	1.9	1.4	004	x	Data Blocks and Texts
2.	<u>Memory extension</u>						
Data Block	Put and Get (transfer media)	002	1.6	1.4	001	x	see also Texts
	0...3999, up to 383 registers	003	1.6	1.4	003	x	
	4000 to 7999, up to 16 KByte	003	1.8	1.4	003	C	with PCD7.R3..
	Transfer of DB's over S-BUS	----	1.9	1.4	004	x	
Memory	Extension : up to 428 KByte	003	1.7	1.21	002	C	with PCD7.R3..
	Memory extension security	003	1.8	1.4	003	C	for DB's structure
Texts	Put and Get (transfer media)	002	1.6	1.4	001	x	see also Data Blocks
TFR	Transfer Data (new inst.)	003	1.7	1.4	002	x	see new instructions
TFRI	New indirect instruction	003	1.9	1.4	004	x	see new instructions
3.	<u>Communication</u>						
S-BUS	Level 1 up to 38.4 KBd	---	1.7	1.21	002	C	
	Level 2 up to 38.4 KBd	---	1.7	1.21	002	C	PCD8.K111 required
	up to 38.4 KBd optimised	---	---	---	---	---	not implemented
	Broadcasting PCD as master	---	1.6	1.21	003	x	
	Broadcasting PC as master	---	1.9	1.21	003	x	with the Debugger
	Data Mode	---	---	---	---	---	not implemented
	Download Configuration	---	2.0	1.4	\$41	x	new switches
	Gateway	---	---	---	---	---	not implemented
	Modem : Private line	---	1.7	1.21	001	x	
	Public line	---	1.7	1.3	002	x	
	Reset/Init string	---	1.9	1.3	004	x	
	Modem +	---	1.9	1.3	004	x	
	Radio modem up to 4'800 Bd	---	1.9	1.21	004	x	TFUL mechanism
	RIO functionality	---	---	---	---	---	not implemented
	SRXM extension	003	1.9	1.4	004	x	
	STXMI and SRXMI	003	1.9	1.4	004	x	to transfer DB's
	Write station number (Debug.)	003	1.9	1.21	004	x	
	XOB 17,18,19	---	1.9	1.4	004	x	

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3.	<u>Communication</u> (continued)						
LAN2		---	---	---	---	---	not implemented
LON	Interface PCD7.F8x0	---	---	---	---	---	not implemented
MC4	Character Mode for RS 485	002	1.6	1.4	001	x	only on interface no.1
MD/SD	Master/slave (full duplex)	002	1.6	1.21	001	x	
MM4	LAC network (Compex)	002	1.6	1.4	001	x	
PROFIBUS	DP : master	---	---	---	---	---	not implemented
	slave	---	---	---	---	---	not implemented
	FMS : only with PCD7.F700	---	---	---	---	---	not implemented
	all indirect instructions	---	---	---	---	---	not implemented
	new functions	---	---	---	---	---	not implemented
	FMS + (GA profile)	---	---	---	---	---	not implemented
4.	<u>General</u>						
Download	Improvement of the procedure	---	2.0	1.4	\$41	x	new switches
EEPROM	Configuration memory	---	---	---	---	---	not implemented
Error Flag	New Error Flag concept	---	---	---	---	---	not implemented
Flash EPROM	User program : Up to 1 MBit	---	---	---	---	---	not implemented in the
	Up to 4 MBit	---	---	---	---	---	memory PCD7.R3..
Graftec	2'000 steps and transitions	002	1.6	1.21	001	x	
	RSB instruction corrected	002	1.6	1.21	001	x	> 25 sequential blocks
	RSB instruction corrected	002	1.6	1.21	004	x	if step call do not exist
History	Improvement of the History list	002	1.6	1.21	001	x	more diagnostic info.
Interrupt	XOB 20 and XOB 25	---	---	---	---	---	not implemented
Password		003	1.9	1.3	004	x	
RTC	Calculation of nb. of week/day	---	---	---	---	---	not implemented
Startup	New Startup routine	N/A	N/A	N/A	004	x	for extra security
User progr.	Up to 428 KByte (2x1 MBit)	003	1.7	1.21	003	x	RAM/EPROM => R3..
XOB	Priority Mechanism (4 levels)	N/A	N/A	N/A	001	x	level 4 is the highest
XOB 7	System Overload for XOB's	---	---	---	---	---	not implemented
XOB 14/15	see instruction SYSWR	---	---	---	---	---	not implemented

X not depending of the hardware version.

N/A not applicable : version number is not relevant for this feature.

PCA2 used hardware **version D** or upward to be compatible with PCA2.

1. used always the latest firmware version of the PCD8P.100 (V003 - September 1996).

2.